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# **Reconfigurable Logic Devices Circuits And Systems Band 48 By Pierre Emmanuel Gaillardon**

**analog reconfigurable circuits  
international journal of. international  
journal of knowledge based and  
intelligent. the rfet a reconfigurable  
nanowire transistor and its. emerging  
reconfigurable nanotechnologies can  
they support. pdf reconfigurable  
systems for multifunctional electronics.  
reconfigurable systems for**

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**multifunctional electronics. us5812756a  
network controller with reconfigurable.  
reconfigurable silicon nanowire devices  
and circuits. all photonic multifunctional  
molecular logic device. the rfet a  
reconfigurable nanowire transistor and  
its. journal of circuits systems and  
puters vol 27 no 06. iet digital library iet  
circuits devices amp systems. theme 2  
design of integrated devices circuits  
and. constructing dynamic multiple  
input multiple output logic. die stacked  
memory device with reconfigurable  
logic. reconfigurable mems enabled rf  
circuits for spectrum sensing.  
reconfigurable logic circuits for**

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intelligent human sensing. edics ieee  
cas. integrated photonic electronic  
circuits and systems justia. device and  
circuit design challenges in the digital.  
contributed pap e r high  
speedreconfigurable. iet circuits devices  
amp systems. cntfet modeling and  
reconfigurable logic circuit design.  
reconfigurable nanowire electronics a  
review solid. reconfigurable puting for  
digital signal processing a. working  
prototype of an optoelectronic xor or  
yes. reconfigurable plementary logic  
circuits with ambipolar. reconfigurable  
ion gating of 2h mote2 field effect. iet  
digital library iet circuits devices amp

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**systems. reconfigurable nanowire  
electronics a review sciencedirect.  
reconfigurable integrated circuits.  
electrical and puter engineering. brain  
inspired puting with memristors  
challenges in. reconfigurable frequency  
multiplication with a. programmable  
logic devices electronics tutorial. proj 7  
hardware software runtime environment  
for. memristor device engineering and  
cmos integration for. petri nets mapping  
into reconfigurable logic controllers.  
optoelectronic logic functions based on  
reconfigurable sic. cntfet modeling and  
reconfigurable logic circuit design.  
graphene the ultimate switch spectrum**

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ieee. programmable hybrid circuit based  
on reconfigurable spp.  
us20150155876a1 die stacked memory  
device with. integrated circuit  
microprocessor circuits britannica. iet  
circuits devices amp systems home icm.  
b485 reconfigurable logic devices  
circuits and systems. application of  
reconfigurable logic technology to.  
programmable circuits ieee conferences  
publications

*analog reconfigurable circuits*

*international journal of*

*May 11th, 2020 - analog reconfigurable  
circuits analog reconfigurable circuits*

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*malcher andrzej falkowski piotr 2014 03 01  
00 00 00 the aim of this paper is to present  
an overview of a new branch of analog  
electronics represented by analog  
reconfigurable circuits the reconfiguration of  
analog circuits has been known and used  
since the beginnings of electronics but the  
universal reconfigurable*

**circuits"**international journal of  
**knowledge based and intelligent**  
**June 7th, 2020 - international journal of**  
**knowledge based and intelligent**  
**engineering systems volume 12 issue 3**  
**international journal of knowledge**  
**based and intelligent engineering**  
**systems vol 12 the popularity of**

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**reconfigurable logic devices and portable hardware demands ever increasing power saving schemes for low power designs'**

**'the rfet a reconfigurable nanowire transistor and its**

**June 11th, 2019 - this article gives a review on the rfet basics and current status in the first sections state of the art of reconfigurable devices will be summarized and the rfet will be introduced together with related devices based on silicon nanowire technology the device optimization with respect to device symmetry and performance will**

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be discussed next" emerging  
reconfigurable nanotechnologies can  
they support

May 22nd, 2020 - 4 1 2 layout for  
reconfigurable logic gate the layouts for  
reconfigurable logic gate based on  
silicon nanowire based rfets were  
proposed based on advanced node  
design rules and the lef layout abstract  
files for place amp route the minority  
gate layout shown in figure 3 is a  
reconfigurable ready layout depending'  
'pdf reconfigurable systems for  
multifunctional electronics

April 24th, 2020 - reconfigurable  
systems plement the existing efforts of

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**miniaturizing integrated circuits to provide a new direction for the development of future electronics"reconfigurable systems for multifunctional electronics**

June 3rd, 2020 - reconfigurable systems complement the existing efforts of miniaturizing integrated circuits to provide a new direction for the development of future electronics such systems can integrate low'

**'us5812756a network controller with reconfigurable**

**May 10th, 2020 - a network controller card having a processor memory and program logic the program logic can be**

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**used as munications circuits and as testing circuits a reconfigure signal from a workstation remotely located from the network controller card initiates the processor to change the program logic from munications circuits to testing circuits and vice versa'**

**'reconfigurable silicon nanowire devices and circuits**

**June 7th, 2020 - reconfigurable silicon nanowire devices and circuits**

**opportunities and challenges walter m weber jens trommer matthias grube**

**namlab ggmbh 01187 dresden germany**

**walter weber namlab andré heinzig**

**markus könig thomas mikolajick**

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**institute for nanoelectronic materials  
and center for advancing electronics  
dresden cfaed'**

**'all photonic multifunctional molecular  
logic device**

**April 23rd, 2020 - photochromes are  
photoswitchable bistable chromophores  
which like transistors can implement  
binary logic operations when several  
photochromes are bined in one  
molecule interactions between them  
such as energy and electron transfer  
allow design of simple boolean logic  
gates and more plex logic devices with  
all photonic inputs and outputs'**

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**'the rfet a reconfigurable nanowire transistor and its**

**May 27th, 2019 - the first work to report on logic soft wired circuit design of polarity controllable or reconfigurable devices was shown by o connor et al 57 59 who demonstrated the possibility to produce user configurable dynamic logic blocks out of double gated carbon nanotube transistors cntfets"journal of circuits systems and puters vol 27 no 06**

**April 6th, 2020 - design of multi context reconfigurable logic controllers implemented in fpga devices oriented for further partial reconfiguration**

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**remigiusz wisniewski and iwona  
grobelna'**

**'iet digital library iet circuits devices  
amp systems**

**June 3rd, 2020 - reduction of drain  
induced barrier lowering in dm hd na  
gaafet for rf applications author s amit  
kumar manisha pattanaik pankaj  
srivastava kamal kishor jha source iet  
circuits devices amp systems volume 14  
issue 3 p 270 275 doi 10 1049 iet cds  
2019 0306 type article show details hide  
details p 270 275 6 in this research work  
a dual metal hetero dielectric with'**

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**'theme 2 design of integrated devices  
circuits and**

**May 26th, 2020 - theme 2 design of  
integrated devices circuits and systems  
cdsi themes mems microponents  
microgenerators non uniform sampling  
and signal processing algorithms  
architectures circuits reconfigurable  
asynchronous logic safe and secured  
robust asynchronous circuits smart  
cmos vision sensors band spectral  
content of a source"constructing  
dynamic multiple input multiple output  
logic**

June 2nd, 2020 - investigation of putting  
devices with dynamic architecture which

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makes devices have reconfigurable ability is an interesting research direction for designing the next generation of puter chip in this paper we present a window threshold method to construct such dynamic logic architecture here dynamic multiple input multiple output mimo logic gates are proposed analyzed and implemented'

## **'die stacked memory device with reconfigurable logic**

April 20th, 2020 - a puter readable storage medium storing code that is operable to manipulate at least one puter system to perform a portion of a process to fabricate

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an integrated circuit package comprising a die stacked memory device comprising a set of one or more stacked memory dies implementing memory cell circuitry and a set of one or more logic dies electrically coupled to'

**'reconfigurable mems enabled rf circuits for spectrum sensing**

**May 21st, 2020 - 1now at sandia 2now at intel 3now at cirrus logic 4now at ibm 5now at ucla tamal ece cmu edu abstract the desire for low power multifunction radios for the dod is driving interest in reconfigurable rf architectures while software reconfiguration is the ultimate**

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**goal data conversion power and dynamic range limitations imply that reconfigurable logic circuits for intelligent human sensing**

May 19th, 2020 - a reconfigurable logic device is a kind of lsi that can change its logic function configuration at run time using reconfigurable devices we can implement any custom circuit regardless of quantity field programmable gate array fpga is a kind of reconfigurable logic device the advances in **edics ieee cas**

**June 7th, 2020 - acs400 circuits and systems for biomedical applications life science and biology acs400a0 circuits for electronics biology fusion acs400a5**

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**medical circuits and systems acs400b0  
circuits for wearable implantable bio  
electronics acs400b5 neuromorphic  
circuits and systems acs410 circuits for  
energy harvesting'**

***'integrated photonic electronic circuits  
and systems justia***

*April 16th, 2020 - photonic interconnect  
reconfigurably couples integrated circuits  
such as microprocessor memory or other  
logic ponents detector modulator broad  
band coupler and waveguide elements  
provide transmit and receive capability on  
cmos substrate puter implemented design  
software and reusable ponent library*

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*automate photonic and circuit design and simulation for manufacturability'*

**'device and circuit design challenges in the digital**

*May 30th, 2020 - o c akgun and y leblebici weak inversion performance of cmos and dcvsbg logic families in sub 300 mv range in proceedings of the ieee international symposium on circuits and systems iscas 06 pp 1251 1254 island of kos greece may 2006"*

**paper high speedreconfigurable**

May 29th, 2020 - high speed reconfigurable integrated circuits ics present many advantages for the systems that they construct and enable higher operating

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frequencies and larger band widths expand the scope and application of reconfigurable systems while maintaining or expanding upon their typical benefits lowcost fastprototyping andimprovedeffi"**iet circuits devices amp systems**

*May 7th, 2020 - circuits devices amp systems vol 11 no 3 pp 274 279 5 2017 sought to realize reconfigurable logic fabrics such as fpga devices in conventional fpga designs the use cnt acts as a semiconductor with small band gap integers  $n$  and  $m$  are the chirality of the tube"***cntfet modeling and reconfigurable logic circuit design**

March 10th, 2020 - abstract this paper

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examines aspects of design technology required to explore advanced logic circuit design using carbon nanotube field effect transistor cntfet devices an overview of current types of cntfets is given and highlights the salient characteristics of each compact modeling issues are addressed and new models are proposed implementing 1 a physics based calculation of energy" **reconfigurable nanowire electronics a review solid**

*April 17th, 2020 - reconfigurable nanowire electronics a review skillfully implemented multifunctional devices that bear highly adaptable logic and putational blocks hold the promise of further advancing electronics*

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*and moore s law 1 even beyond the end of classical scaling 2 4*"reconfigurable  
puting for digital signal processing a  
December 28th, 2019 - steady advances  
in vlsi technology and design tools have  
extensively expanded the application  
domain of digital signal processing over  
the past decade while application  
specific integrated circuits asics and  
programmable digital signal processors  
pdspds remain the implementation  
mechanisms of choice for many dsp  
applications increasingly new system  
implementations based on  
reconfigurable"working prototype of an  
optoelectronic xor or yes

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**May 31st, 2020 - other chemical logic systems can process information encoded in light magnetic field electric signals or changes in physical parameters like temperature or pressure and give the output in any of the abovementioned forms 1 in the case of non electric or non optical input and output signals the integration of chemical logic devices with currently used electronic'**

***'reconfigurable plementary logic circuits with ambipolar***

*February 2nd, 2017 - the molecular structure and the energy band diagram of the ambipolar semiconducting polymer*

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*pdpp3t are shown in fig 1c non planar ambipolar offts offer the simple fabrication of ambipolar anic reconfigurable complementary logic circuits author contributions h y designed and fabricated the transistor devices and circuits'*

## **'reconfigurable ion gating of 2h mote2 field effect**

*November 27th, 2019 - transition metal dichalcogenides tmds are two dimensional 2 d atomically thin crystals of broad interest for use in field effect transistors fets tunnel field effect transistors tfets and optoelectronic devices there are a growing number of experimental demonstrations of*

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*tmd fets in materials such as mos 2 wse 2 and mote 2 for tfets tmds with a narrow band gap such as wse 2 and'***iet digital library iet circuits devices amp systems May 6th, 2020 - effect of 1 f noise in integrating sensors and detectors author s t meyer r e johanson s kasap source iet circuits devices amp systems volume 5 issue 3 p 177 188 doi 10 1049 iet cds 2010 0220 type article show details hide details p 177 188 12 the authors calculate the variance in the output of an integrating sensor or detector when in the presence of 1 f ? noise in"reconfigurable nanowire electronics a review sciencedirect**

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**May 27th, 2020 - the functional extension of switching elements is an alternative approach to classical scaling towards conceiving circuits and systems with added putational value skillfully implemented multifunctional devices that bear highly adaptable logic and putational blocks hold the promise of further advancing electronics and moore s law 1 even beyond the end of classical scaling 2 3'**

***'reconfigurable integrated circuits***

*June 4th, 2020 - reconfigurable integrated circuits munications systems reffers to the need to offer to users plete access through*

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*a single mobile device figure 2 parison of hardware resources between a tri band and a reconfigurable receiver'* **electrical and puter engineering**

*June 8th, 2020 - embedded systems reconfigurable puting partial and dynamic reconfiguration on fpgas special purpose architectures hardware software co design data mining machine learning pattern analysis and recognition fpga and asic design hardware security system on chip puter architecture and vlsi systems'* **'brain inspired puting with memristors challenges in**

*June 7th, 2020 - this article provides a review of current development and*

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*challenges in brain inspired computing with memristors we review the mechanisms of various memristive devices that can mimic synaptic and neuronal functionalities and survey the progress of memristive spiking and artificial neural*

*networks"***reconfigurable frequency multiplication with a**

**May 18th, 2020 - a single ferroelectric field effect transistor which is made from ferroelectric hafnium oxide can be used as a full wave rectifier and frequency doubler'**

**'programmable logic devices electronics tutorial**

*June 3rd, 2020 - in fixed logic devices user*

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can implement the fixed binational or sequential circuit in the other hand in programmable logic device user can implement various functions programmable logic device implements wide range of logic functions figure below shows a fixed logic circuit of and and or gate this circuit produces an output that is" **proj 7 hardware software runtime environment for** May 25th, 2020 - project title hardware software runtime environment for reconfigurable puters brief introduction this project presents a code sign that is borph that is hw sw considering operating system made for fpga based reconfigurable personal puters'

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***'memristor device engineering and cmos integration for***

*May 13th, 2020 - as such memristors could be used in reconfigurable logic circuits 2 or in neuromorphic systems 3 in order to mimic the synapses which are the fundamental key ponents of neural systems"***petri nets mapping into reconfigurable logic controllers**

August 11th, 2017 - 10 m adamski  
behavioural specification of programs for modular reconfigurable logic controllers  
proceedings of the mixed design of integrated circuits and systems mixdes  
2006 gdynia poland pp 239 244 11 m  
adamski reconfigurable logic controller for

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embedded applications'

**'optoelectronic logic functions based on reconfigurable sic**

*May 19th, 2020 - wdm multilayered sic si devices based on a si h and a sic h filter design are approached from a reconfigurable point of view results show that the devices under appropriated optical bias act as reconfigurable active filters that allow optical switching and optoelectronic logic functions development'*

**'cntfet modeling and reconfigurable logic circuit design**

**May 12th, 2020 - this paper examines**

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**aspects of design technology required to explore advanced logic circuit design using carbon nanotube field effect transistor cntfet devices an overview of current types of cntfets is given and highlights the salient characteristics of each pact modeling issues are addressed and new models are proposed implementing 1 a physics based calculation of energy conduction'**

**'graphene the ultimate switch spectrum  
ieee**

May 3rd, 2020 - with luck we might see graphene based reconfigurable logic prototypes within the next five years for

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building logic capable of replacing cmos circuits it won't be a moment too soon about the "**programmable hybrid circuit based on reconfigurable spp**

**May 1st, 2020 - the reconfigurable spoof spp some devices with adjustable frequency and nonlinear phenomena are realized 32 37 further than that three different digital analog functionalities can be realized by coding representation in a simple programmable system 38 and a dual channel spoof spp logic and gate was'**

**'us20150155876a1 die stacked memory device with**

**April 18th, 2020 - a die stacked memory**

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**device incorporates a reconfigurable logic device to provide implementation flexibility in performing various data manipulation operations and other memory operations that use data stored in the die stacked memory device or that result in data that is to be stored in the die stacked memory device one or more configuration files representing corresponding logic'**

**'integrated circuit microprocessor circuits britannica**

June 7th, 2020 - integrated circuit  
integrated circuit microprocessor circuits  
microprocessors are the most plicated ics

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they are posed of billions of transistors that have been configured as thousands of individual digital circuits each of which performs some specific logic function a microprocessor is built entirely of these logic circuits synchronized to each other'

**'iet circuits devices amp systems home icm**

**September 10th, 2015 - millimetre wave devices bicmos integrated circuits heterojunction bipolar transistors microwave switches sige reconfigurable bicmos uniform delay crossbar switch broadband application wide tuning range narrowband application wide band crossbar switch ibm 8hp sige**

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**heterojunction bipolar transistors  
symmetrical signal path design binary  
signals phased array antenna system  
phase noise 8hp'**

**'b485 reconfigurable logic devices  
circuits and systems  
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you can digest the remendation easily**

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**from that book"application of reconfigurable logic technology to**

June 4th, 2020 - 2 hardware specialization with reconfigurable circuits in the previous paragraph we discussed customizing logic circuits for an application or an algorithm however it is possible to customize the circuit for specific input data this technique is called hardware specialization or data dependent circuit'

***'programmable circuits iee conferences publications***

*May 21st, 2020 - the lascas 2020 symposium will cover novel technical developments in all the areas of the circuits*

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*and systems society but focusing in the areas of biomedical and implantable devices and applications low power integrated circuits high speed munication interfaces and circuits and systems design for renewable energy applications"*

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