
Beyond Loop Level Parallelism In Openmp Accelerators Tasking And More Lecture Notes In Computer Science 6132 Band 6132 By Mitsuhsa Sato Toshihiro Hanawa Matthias S Müller Barbara Chapman Bronis R De Supinski

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June 6th, 2020 - of course openmp is a natural for the development of vector parallel codes but openmp continued to focus on loop level parallelism utilizing threads until the introduction of the simd construct in 2014 until then vectorization was handled either by the piler via auto vectorization or explicitly by the programmer via intrinsics'

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'openmp 4 0 spec adds support for dsps real insidehpc

May 1st, 2020 - besides several major enhancements this release openmp extends its reach beyond pure hpc to include dsps real time systems and accelerators the openmp api aims to provide high level parallel language support for a wide range of applications from automotive and aeronautics to biotech automation robotics and financial analysis'

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May 22nd, 2020 - version 3 0 was released in may 2008 included in the new features in 3 0 is the concept of tasks and the task construct significantly broadening the scope of openmp beyond the parallel loop constructs that made up most of openmp 2 0 version 4 0 of the specification was released in july 2013'

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